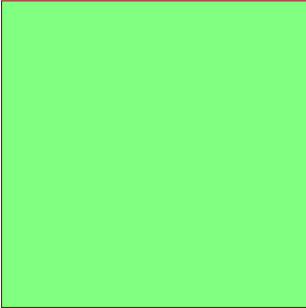
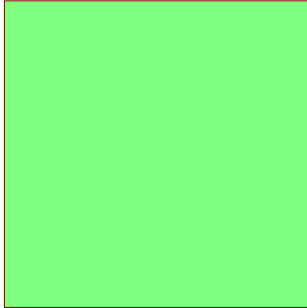


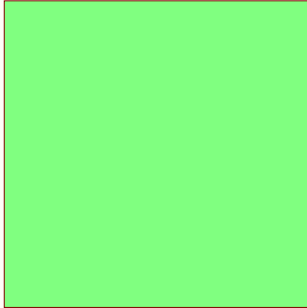
Designator
[01] - COVER PAGE.SchDoc



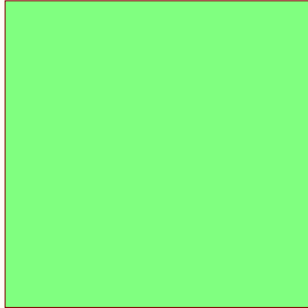
Designator
[02] - BLOCK DIAGRAM.SchDoc



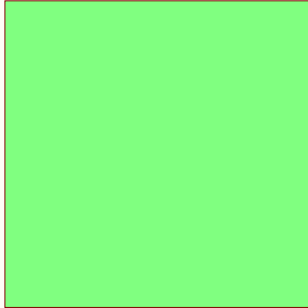
Designator
[03] - MCU.SchDoc



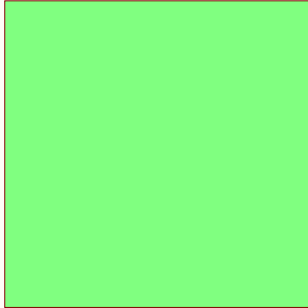
Designator
[04] - IOLINK.SchDoc



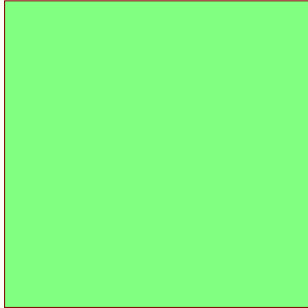
Designator
[05] - MEMORIES.SchDoc



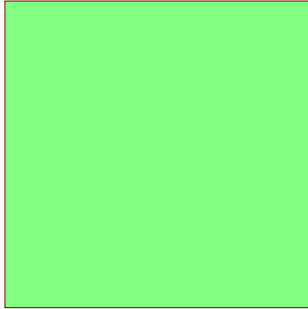
Designator
[06] - RFID.SchDoc



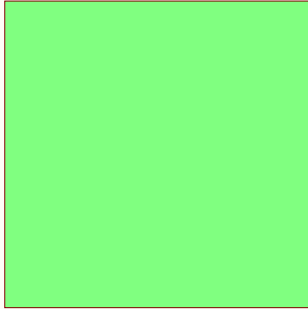
Designator
[07] - SIGNALING.SchDoc



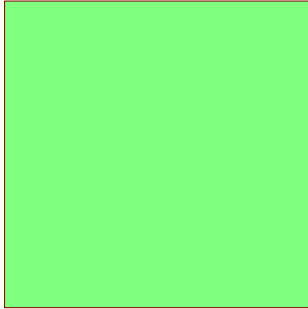
Designator
[08] - POWER.SchDoc



Designator
[09] - CONNECTORS.SchDoc



Designator
[10] - DOC REVISION HISTORY.SchDoc



TEMPLATE NOTES

Set Project Parameters

- 1) Go to Project -> Project Options -> Parameters
- 2) Set Project and Version

Mark Not Fitted Components as

NF

Net Class Example



Differential signal example



TITLE Examples (You can change the color to reflect your company color)

PAGE TITLE

Peripheral / Group of component title

Smaller Ttitle

Schematic Status Explanation

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.

Title 05B24		Kronotech Srl Via Adriatica, 284 33030 - Basaldella (UD) Italy
Size: A3	Revision: V0	
Date: 23/09/2024	Sheet 1 of 11	

05B24

Variant: [00] - Board All Populated

23/09/2024
V0

DRAFT

Page	Index	Page	Index	Page	Index	Page	Index
1	COVER PAGE	11		21		31	
2	BLOCK DIAGRAM	12		22		32	
3	MCU	13		23		33	
4	IO-LINK	14		24		34	
5	MEMORIES	15		25		35	
6	RFID	16		26		36	
7	SIGNALING	17		27		37	
8	POWER	18		28		38	
9	CONNECTORS	19		29		39	
10	DOC REVISION HISTORY	20		30		40	

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes .

DESIGN NOTE:
Example text for critical
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

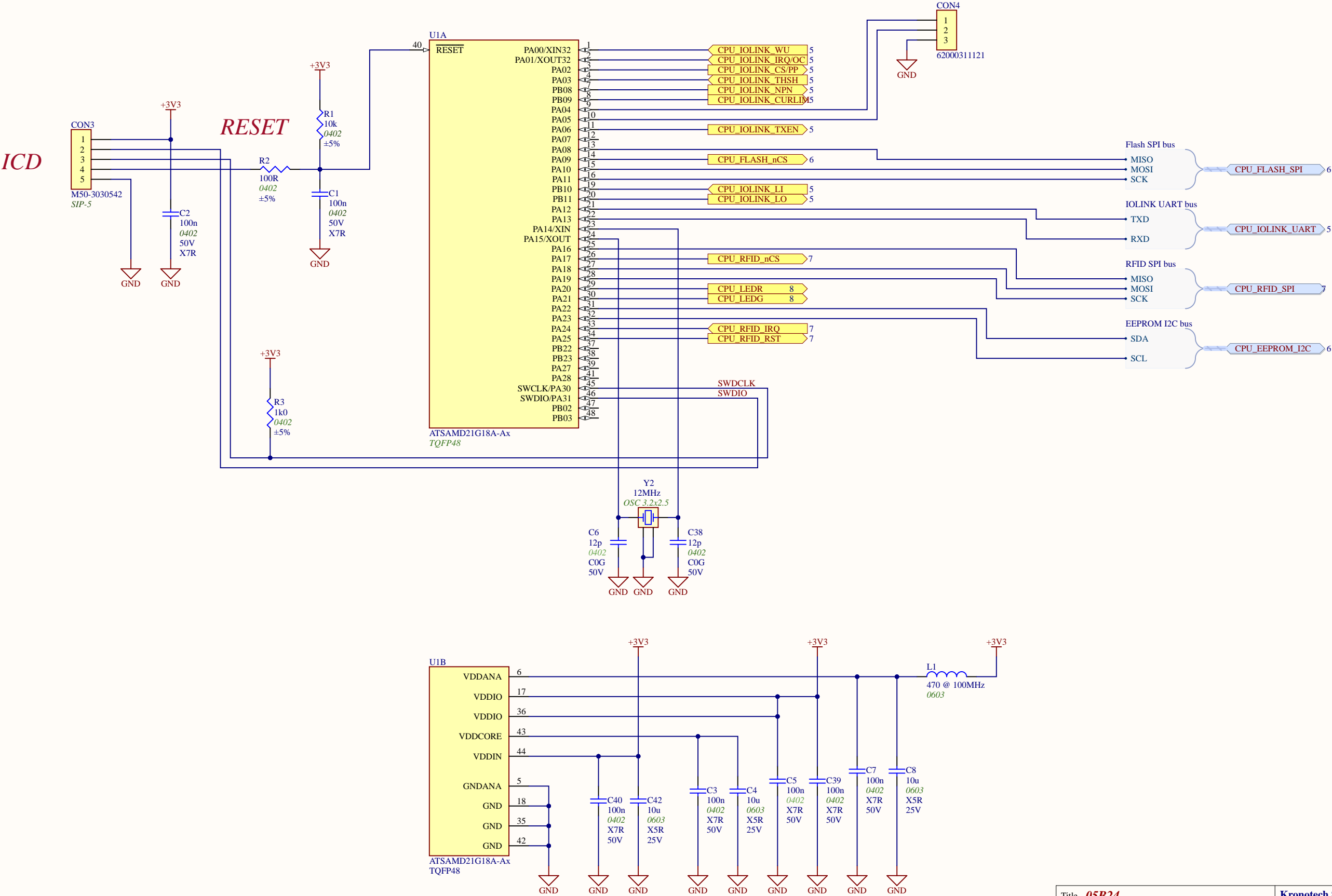
LAYOUT NOTE:
Example text for critical
layout guidelines.

	1	2	3	4	5	6	7	8
A	<div>05B24</div> <div>(Block Diagram)</div>							
B								
C								
D								
	1	2	3	4	5	6	7	8

Title 05B24		Kronotech Srl Via Adriatica, 284 33030 - Basaldella (UD) Italy
Size: A3	Revision: V0	
Date: 23/09/2024	Sheet 3 of 11	

05B24

(MCU)



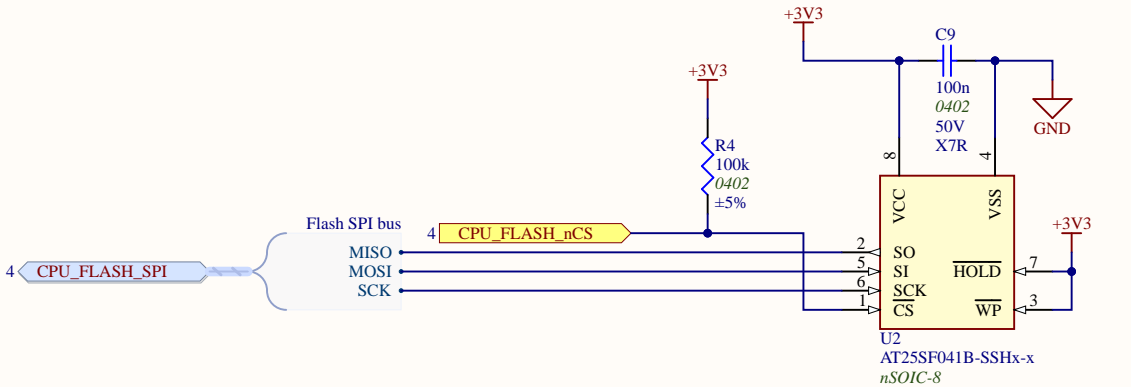
IO-Link



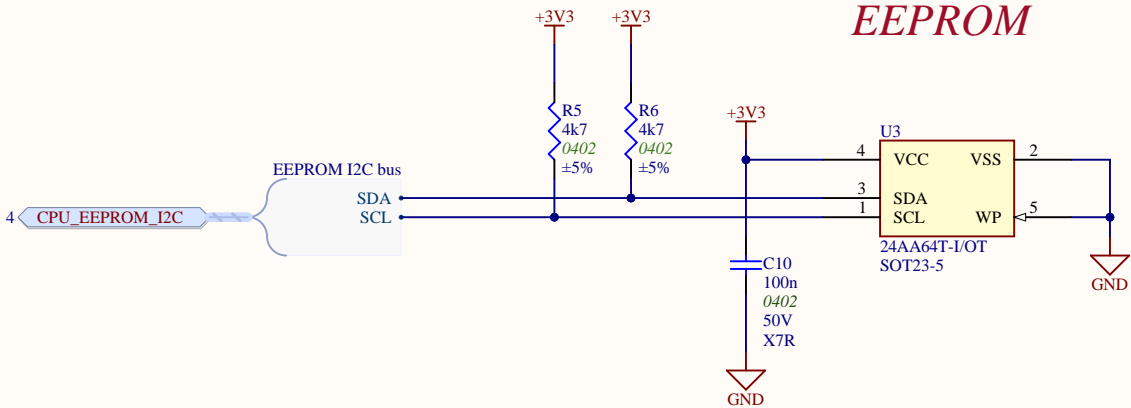
05B24

(Memories)

FLASH MEMORY

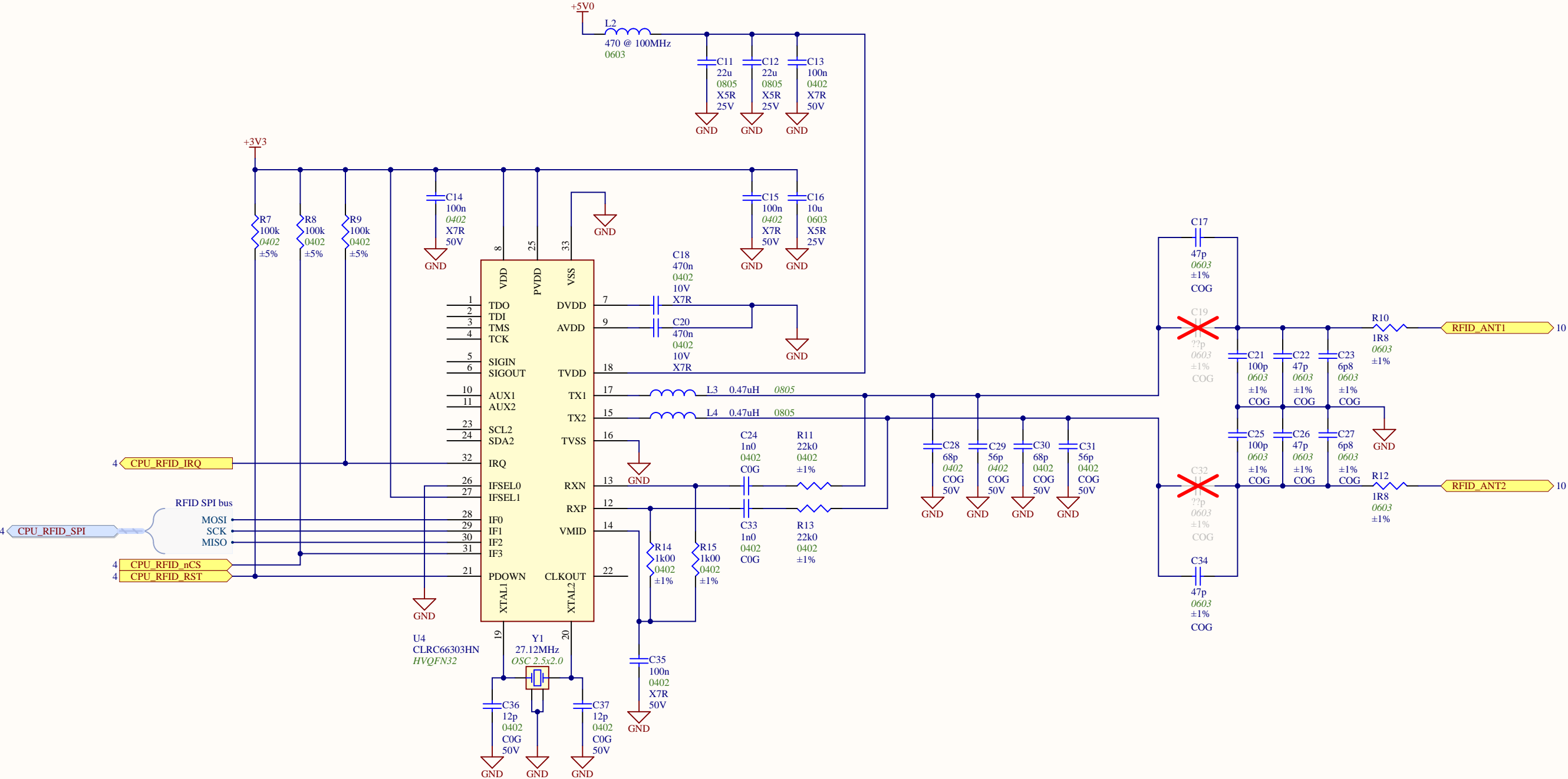


EEPROM



05B24

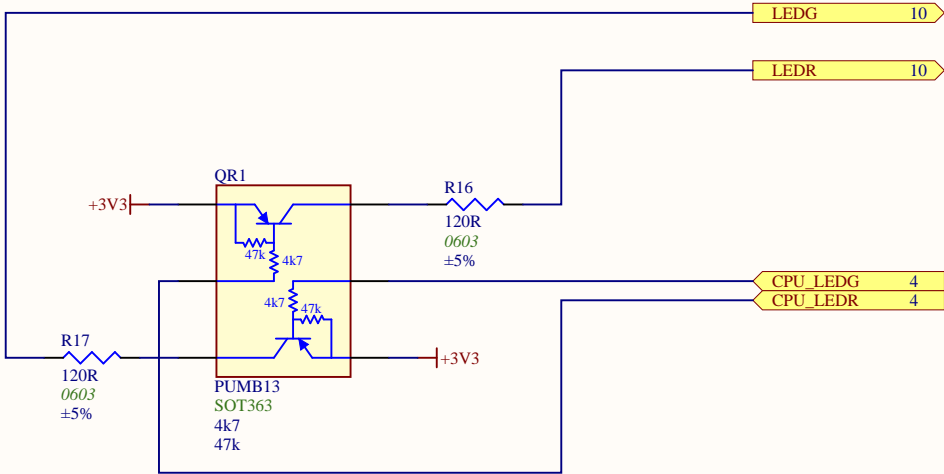
(RFID)



05B24

(Signaling)

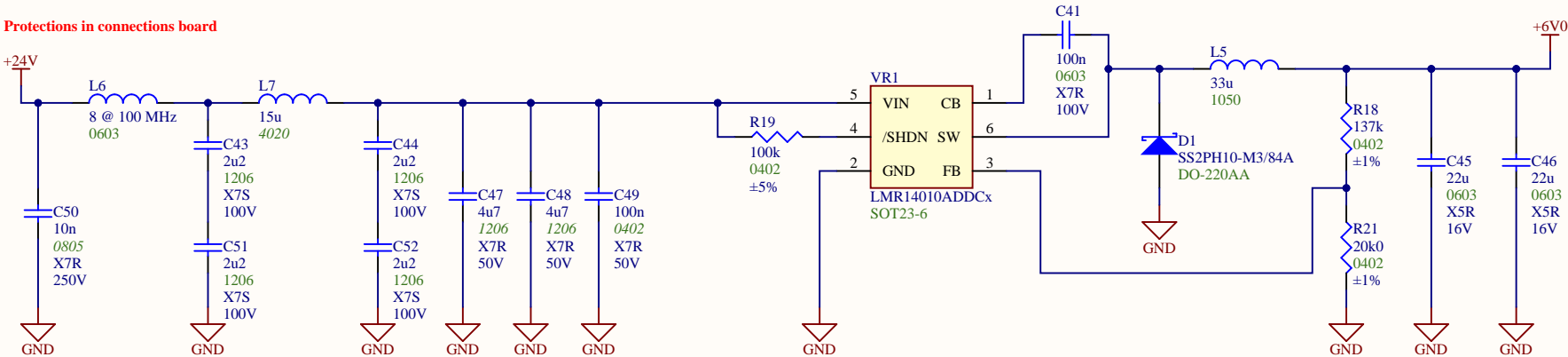
LEDS



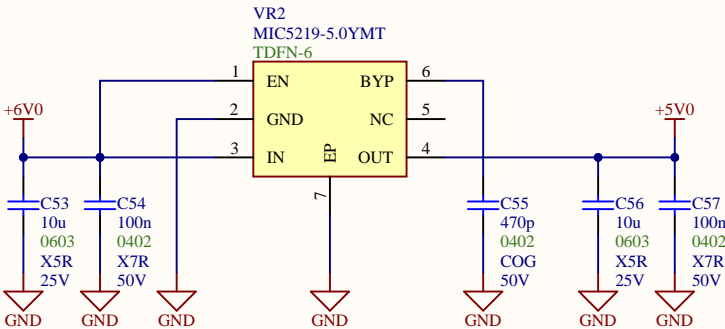
05B24

(Power)

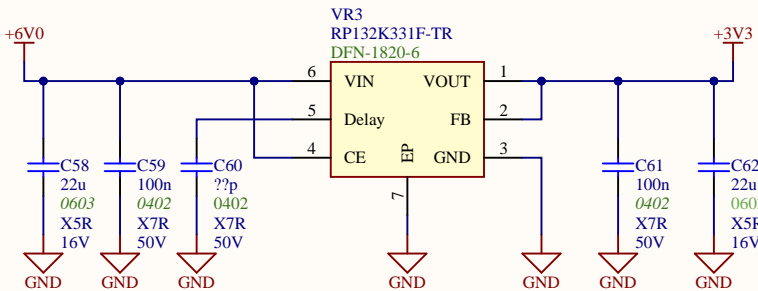
6V0



5V0

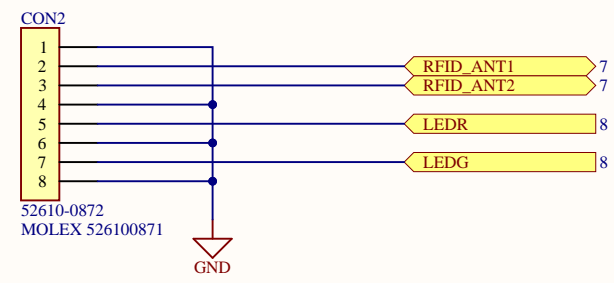
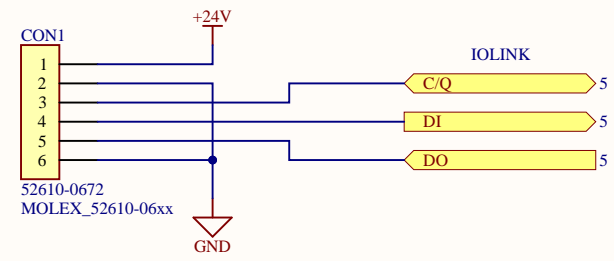


3V3

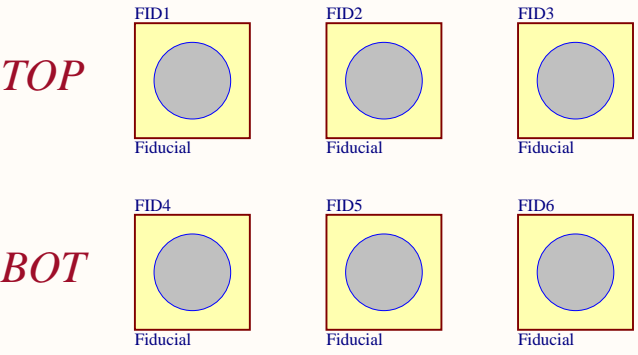


05B24

(Connectors)



FIDUCIALS



1	2	3	4	5	6	7	8	
A	<div>05B24</div> <div>(Doc Revision History)</div> <div>xx/xx/2024 - V0 - Initial release</div>							A
B								B
C								C
D								D
CLOCKS (CPU & PCIe)								
1	2	3	4	5	6	7	8	

Title 05B24		Kronotech Srl Via Adriatica, 284 33030 - Basaldella (UD) Italy
Size: A3	Revision: V0	
Date: 23/09/2024	Sheet 11 of 11	